

CLAIMS

1. A method of propagating signals on interconnect in a programmable logic device, the method comprising:

selecting between source signals to drive a shared interconnect portion; and

coordinating latching of the source signals at respective destinations with the selecting.

2. The method of Claim 1, wherein the source signals are provided by a configurable logic block in the programmable logic device.

3. The method of Claim 1, wherein the source signals are provided by configurable logic blocks in the programmable logic device.

4. The method of Claim 1, wherein the destinations are provided in a configurable logic block in the programmable logic device.

5. The method of Claim 1, wherein the destinations are provided in configurable logic blocks in the programmable logic device.

6. The method of Claim 1, wherein the source signals include non-critical signals.

7. The method of Claim 1, wherein the source signals include critical signals.